**FIG. 1**

Addr	Register Contents	
16	Location Register	<u>120</u>
17	Control Register	<u>118</u>
18	16 Bit Register	
19	16 Bit Register	
20	16 Bit Register	
21	16 Bit Register	
22	16 Bit Register	
23	16 Bit Register	
24	Block Register 0	
25	Block Register 1	
26	Block Register 2	
27	Block Register 3	
28	Block Register 4	
29	Block Register 5	
30	Block Register 6	
31	Block Register 7	

Diagram illustrating the contents of a register set (104). The register set is organized into two groups:

- Group 110:** Includes the Location Register (120) at address 16 and the Control Register (118) at address 17.
- Group 112:** Includes a series of 16-bit registers (addresses 18-23) and a series of block registers (addresses 24-31, labeled Block Register 0 through Block Register 7).

The address range is indicated by 'Addr' on the left, with values from 16 to 31. A bracket on the right indicates the entire set is labeled 104.

**FIG. 2**

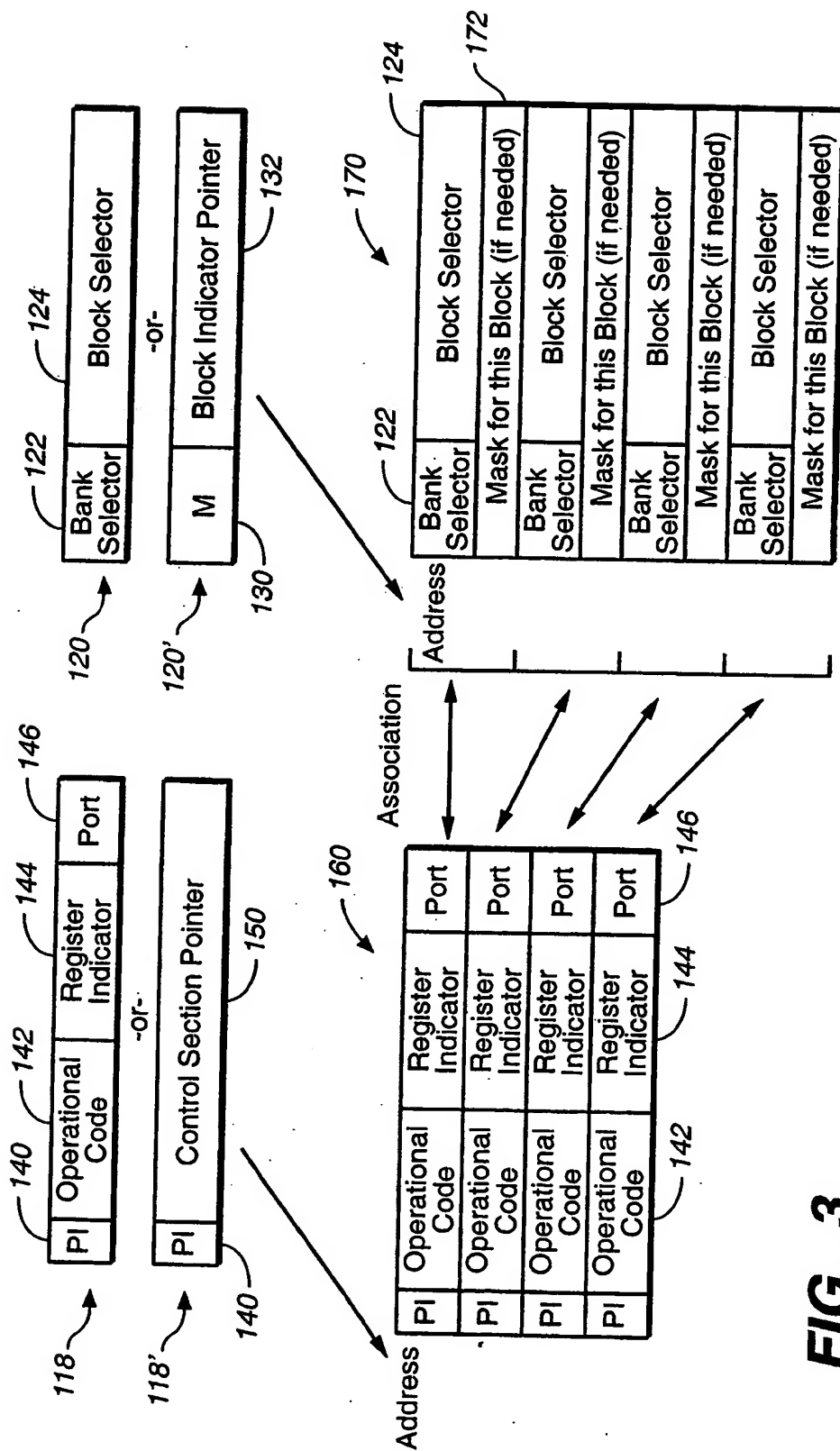


FIG. 3